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|   | Filing Date          |                      | 2005-09-23 |
|   | First Named Inventor | Roger D. Chamberlain |            |
|   | Art Unit             | 2162                 |            |
|   | Examiner Name        | Fleurantin, Jean B.  |            |
| Attorney Docket Number  |                      | 53047-57370          |            |

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| 9  | 7617291 |  | 2009-11-10 | Fan et al.   |  |
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|---|------------|----|----|------------|----------------------|--------------------------|
| 1 | 2000286715 | JP | A  | 2000-10-13 | Toshiba Corp         | <input type="checkbox"/> |
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| 7 | SEKI ET AL., "High Speed Computation of Shogi With FPGA", Proceedings of 58th Convention Architecture, Software Science, Engineering, March 9, 1999, pp. 1-133-1-134                     | <input type="checkbox"/> |
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